# GOVERNMENT ENGINEERING COLLEGE, PALANPUR (061)

*Student Start-Up & Innovation Policy [SSIP 2.0],*
*Institute Innovation Council [IIC] & Design Lab*

Organized

# A Hands-On Workshop for Engineering Minds On Exploring Arduino and NodeMcu



2nd March 2024, Saturday, 11:30 am onwards
Electrical Seminar Hall, 4012

**"Government Engineering College, Palanpur" (GECPL),** operates under the aegis of Government of Gujarat (established in the year 2009). It is affiliated to Gujarat Technological University, Ahmedabad and is also approved by All India Council of Technical Education (AICTE), New Delhi. GECPL provides a dynamic and supportive environment in which faculty and staffs continuously grow intellectually and professionally.

Our Vision is to be a leading technical institute facilitating transformation of human resources into socially responsible engineering professionals for sustainable development. Our mission is to achieve academic excellence by developing state-of-the-art laboratories and academic infrastructure. To create an ecosystem that promotes value based technical education, innovation and entrepreneurship for sustainable development. To contribute to industry and society by providing technical and consultancy services. To enhance technical competencies of human resources by providing need base trainings and quality improvement programs.
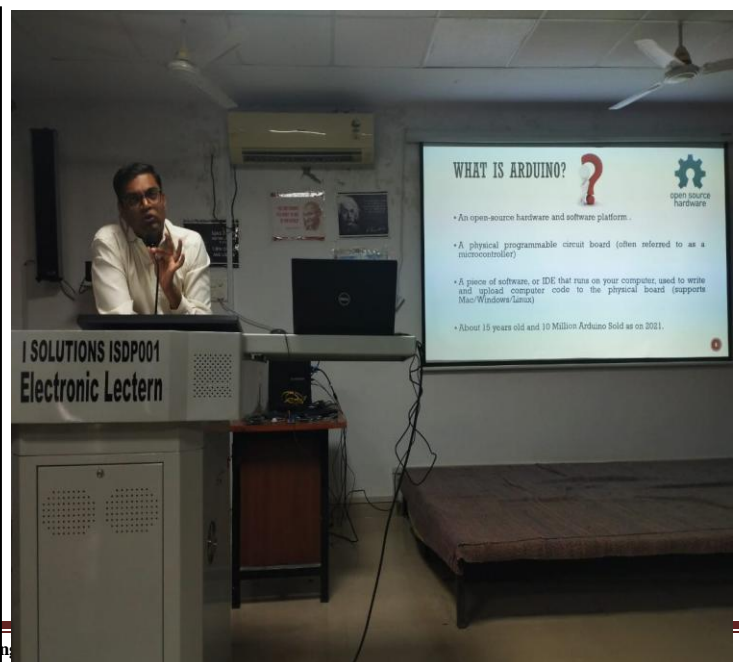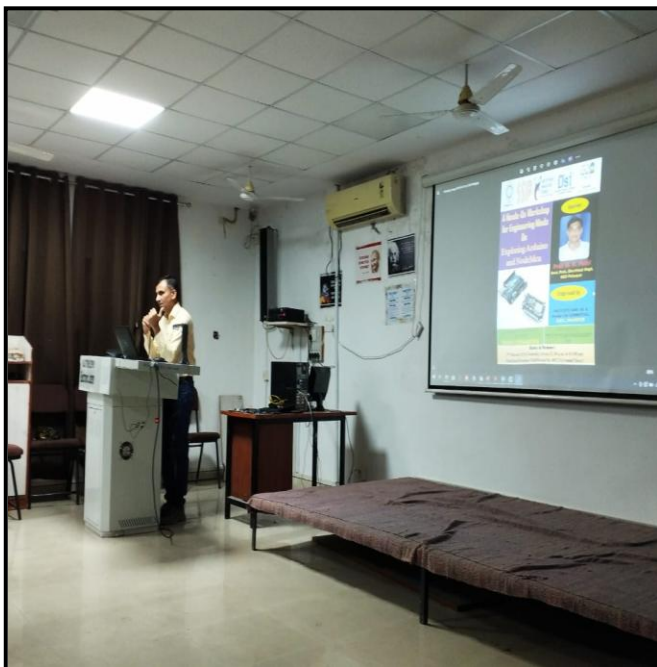
In above context, Student Start-up & Innovation Policy [SSIP 2.0], Institute Innovation Council [IIC] & Design Lab committee of Government Engineering College, Palanpur has organized a **Hands-On Workshop for Engineering Minds on - Exploring Arduino and NodeMcu** on date **2nd March 2024, Saturday** for students, faculties and staff members of Government Engineering College, Palanpur. The aim of organizing this workshop was too aware the students and faculties about the application of Adruino & NodeMcu in various multi-disciplinary field of Engineering..
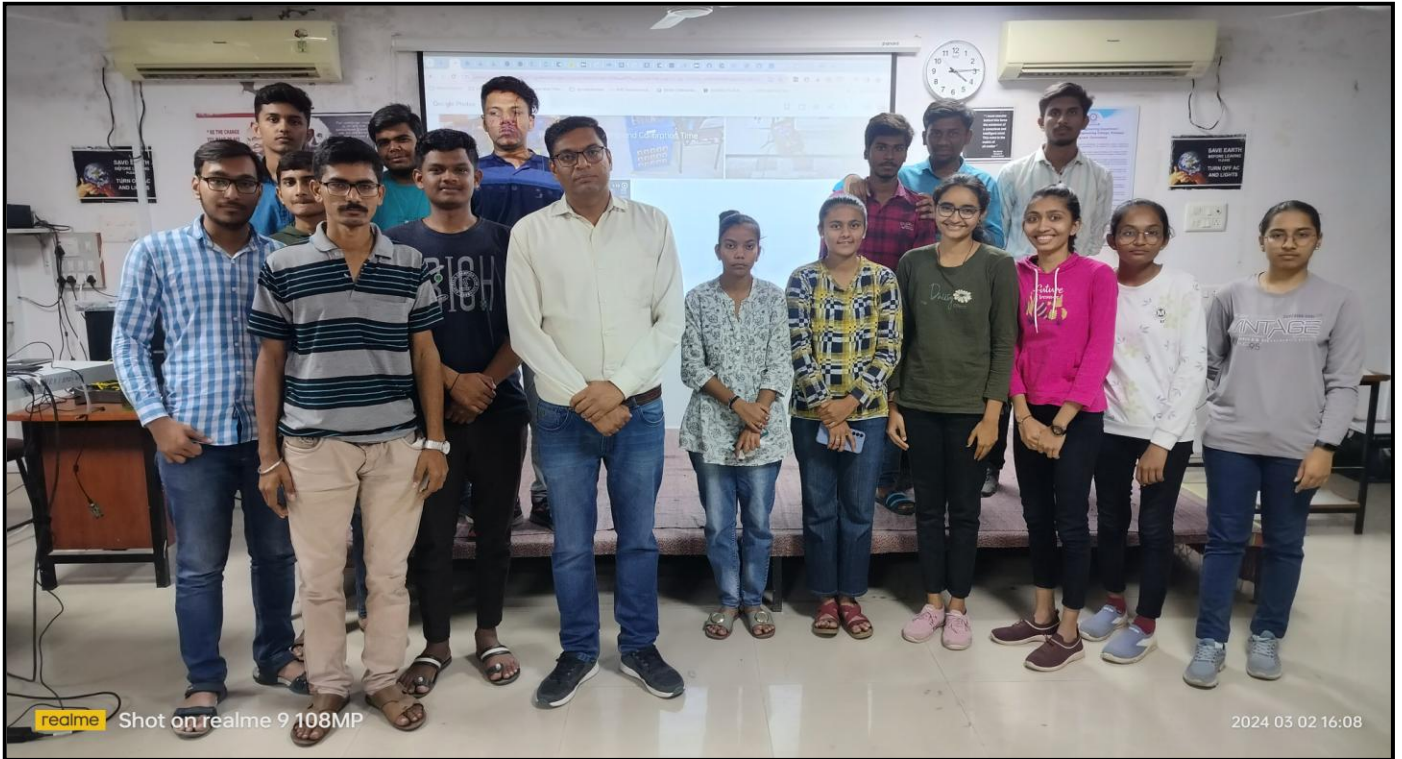
Dr. K. B. Judal, Chairman, SSIP Cell and Principal, Government Engineering College, Palanpur starts the session by encouraging the students to take the benefits of various funds available in SSIP 2.0. Thereafter, Prof. M. K. Patel, Member, SSIP Cell and Assistant Professor, Electrical Engineering Department, Government Engineering College, Palanpur has starts his session on today's Workshop. The session was divided into 2 sections, Lecture and Hand on Practice session. He provides the clear guidance about Adruino and NodeMcu. He had discussed about the applications, components, installation process and demo of few prototypes made using Adruino & NodeMcu. The session make to students to think over the multi-disciplinary benefits in their field of engineering.

At last, the program was concluded by SSIP coordinator, Prof. V. D. Patel.

65 students and more than 15 faculties of various departments had attended the program. The session was found very interesting, informative and thought provoking.

## Photo Gallery

Report Prepared by:

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